

Appl. No. 09/706,095

In the Claims

Please amend the claims as follows:

1. (Original) A method of determining whether an electronic device is simulated, comprising:

5 storing a first value in a memory location;

executing at least one operation on said electronic device wherein said at least one operation causes said memory location to change value if said electronic device is simulated and wherein said at least one operation does not cause said memory location to change value if said electronic device is not simulated; and

10 reading a second value from said memory location to determine whether said second value is different from said first value.

15 2. (Original) The method of claim 1, wherein said electronic device comprises a computer.

3. (Original) The method of claim 1, wherein said memory location comprises a register.

20 4. (Original) The method of claim 3, wherein said register comprises general register 0.

5. (Original) The method of claim 3, wherein said register comprises a return register.

6. (Original) The method of claim 1, wherein said at least one operation comprises a ldil operation.

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7. (Original) The method of claim 1, wherein said at least one operation comprises at least one nop operation.

8. (Original) The method of claim 7, wherein each of said at least one nop operation is followed by a parameter.

5 9. (Currently amended) A method of indicating to program code that it is executing on simulated hardware ~~an electronic device is a simulation~~, comprising:

said simulated hardware reading software instructions from
said program code to be executed;

10 said simulated hardware comparing said software
instructions for said electronic device with a predetermined sequence of instructions; and

15 said simulated hardware setting a flag indicating that program code is executing on simulated hardware ~~said electronic device is a simulation~~ if said software instructions include said predetermined sequence of instructions, wherein said predetermined sequence of instructions, if executed on physical hardware rather than simulated hardware, would not set said flag.

20 10. (Currently amended) The method of claim 9, wherein said simulator hardware comprises a simulated electronic device ~~comprises a computer~~.

25 11. (Currently amended) The method of claim 9, wherein setting said flag ~~indicating that said electronic device is a simulation~~ comprises storing a value in a memory location.

12. (Currently amended) The method of claim 9, wherein said flag is set ~~indicating that said electronic device is a simulation~~ ~~is performed~~ only if said predetermined sequence of instructions in

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said software instructions are not interrupted by another instruction.

13. (Original) The method of claim 9, wherein said predetermined sequence of instructions comprises at least one operator and parameter.

14. (Original) The method of claim 13, wherein said at least one operator and parameter comprises at least one ldil operator and a predetermined number.

15. (Original) The method of claim 13, wherein said at least one operator and parameter comprises at least one nop operator and a predetermined number.

16. (Original) The method of claim 9, wherein said predetermined sequence of instructions comprises a sequence of null operations.

17. (Currently amended) A computer hardware simulator, comprising:
a) at least one computer readable storage medium; and
b) computer readable program code stored in the at least one computer readable storage medium, the computer readable program code comprising:

i) code for receiving instructions written to be executed by ~~for~~ said computer hardware or by said computer hardware simulator which simulates said computer hardware;

ii) code for comparing said instructions with a predetermined set of instructions; and

iii) code for setting a flag if said instructions contain said predetermined set of instructions, wherein said flag indicates that said instructions are executing on said computer hardware simulator ~~is~~

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~~simulated~~ rather than ~~physical~~ said computer hardware.

18. (Original) The computer hardware simulator of claim 17,
wherein said flag comprises a predetermined value stored in a
register.

19. (Original) The computer hardware simulator of claim 17,
wherein said predetermined set of instructions must be included in
said instructions in order with no other intervening instructions
before setting said flag.

20. (Original) The computer hardware simulator of claim 17,
wherein said predetermined set of instructions comprises a plurality
of ldil operations storing predetermined values to general register
0.

21. (Original) The computer hardware simulator of claim 17,
wherein said predetermined set of instructions comprises a plurality
of nop operations with predetermined parameters.

22. (Original) A computer hardware simulator, comprising:
means for receiving an instruction sequence;
means for comparing said instruction sequence with a
predetermined instruction sequence; and
means for indicating that said computer hardware is a
simulation if said instruction sequence matches said predetermined
instruction sequence.

23-24. (Cancelled)